APR 0 1 2005 WARRENO. 22186

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Re: Attorney Docket No. 1054.026

In re application of:

William B. Andrews, Mou C. Lin, and John Schadt

Serial No.:

10/671,363

Group Art Unit:

<u> 2819</u>

Filed:

09/25/03

Examiner:

Daniel D. Chang

Matter No.:

L03-006

Phone No.:

571-272-1801

For:

Programmable I/O Structure for FPGAs and the Like Having Shared Circuitry

AMENDMENT UNDER 37 CFR 1.111

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

This Amendment is filed in response to the office action of 01/07/05.

Certification Under 37 CFR 1.8

Date of Deposit 3/30/05

I hereby certify that this correspondence is being deposited in the United States Postal Service with sufficient postage as first class mail under 37 CFR 1.8 on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

(Name of berson mailing)

(Signature of person mailing)